



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent Application of:  
William Moses et al.

Application No.: 10/809,931

Confirmation No.: 6409

Filed: March 25, 2004

Art Unit: 2878

For: MULTI-CHANNEL DETECTOR READOUT  
METHOD AND INTEGRATED CIRCUIT

Examiner: Q. T. Le

**INFORMATION DISCLOSURE STATEMENT (IDS)**

MS RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08A and PTO/SB/08B. It is respectfully requested that the information expressly be considered during the prosecution of this request for continued examination application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed after the mailing date of a Notice of Allowance and Fee(s) Due, which occurred prior to a now filed Request For Continued Examination (RCE) with payment, and before any payment of the Issue Fee (37 CFR 1.97(d)) was made. Applicants accordingly request that this Information Disclosure Statement now be considered.

A copy of each foreign patent reference listed on the PTO/SB/08A and each nonpatent literature reference listed on the PTO/SB/08B is attached.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure Statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that this Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any fee that may be due for this filing to Deposit Account No. 06-2375, under Order No. LBNL 214US1/10413934. A duplicate copy of this paper is enclosed.

Dated: October 28, 2005

Respectfully submitted,

By 

Thomas S. Hahn

Registration No.: 30,845

FULBRIGHT & JAWORSKI L.L.P.

801 Pennsylvania Avenue, N.W.

Washington, DC 20004-2623

(202) 662-0200

(202) 662-4643 (Fax)

Attorney for Applicant

Approved for use through 07/31/2006. OMB 0651-0031  
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(Use as many sheets as necessary)*

Sheet	1	of	1
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**Complete if Known**

Application Number	10/809,931
Filing Date	March 24, 2004
First Named Inventor	William W. Moses
Art Unit	2878
Examiner Name	Que Tan Le
Attorney Docket Number	IB-1472A/LBNL 214US1

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	MM-DD-YYYY			
		EP 0157141 A	09/10/1985	PROGE		
		WO 8403810 A	09/27/1984	FORCHEIMER		
		EP 0260858 A	03/23/1988	CANON		

Examiner Signature		Date Considered	
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1

of

1

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Application Number	10/809,931
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Examiner Name	Que Tan Le
Attorney Docket Number	IB-1472A LBNL 214US1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		D. PANOGIOTOPOULOS, "Microprocessing and Microprogramming," The VLSI Design of a Two Dimensional Image Processing Array, Vol. 14 (No. 3), p. 125-132, (October 1, 1984)	
		Translation of Abstract of Case Number EU0157141A1	

Examiner  
SignatureDate  
Considered

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